



D. J. Jones
#9 6-17-98
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PATENT

Attorney Docket No.: HELLO-00300

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Bruce W. Stelman

Serial No.: 08/625,398

Filed: March 27, 1996

For: **SMART INTERFACE
TECHNOLOGY**

) Group Art Unit: 2742

) Examiner: Jacques M. Saint-Surin

) TRANSMITTAL LETTER

) 260 Sheridan Avenue, Suite 420
) Palo Alto, CA 94306
) (650) 833-0160

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Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Enclosed please find an additional Supplemental Information Disclosure Statement and Form PTO-1449, including a copy of the references contained thereon, for filing in the U.S. Patent and Trademark Office.

Applicant previously submitted check #7192 for the amount of \$240.00 pursuant to 37 C.F.R. § 1.17(p) for filing Supplemental Information Disclosure Statements after three months as set forth in 37 C.F.R. § 1.97(c).

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. 08-1275. An originally executed duplicate of this transmittal is enclosed for this purpose.

Respectfully submitted,

HAVERSTOCK & OWENS LLP

By: 

Thomas B. Haverstock
Reg. No.: 32,571

Attorneys for Applicant

Dated: 5-28-98

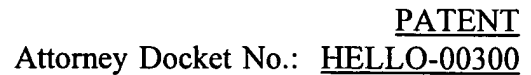
CERTIFICATE OF MAILING (37 CFR § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner for Patents, Washington, D.C. 20231

HAVERSTOCK & OWENS LLP

Dated: 5/29/98

By: 



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In re Application of:

Bruce W. Stelman

Serial No.: 08/625,398

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For: **SMART INTERFACE
TECHNOLOGY**

) Group Art Unit: 2742

) Examiner: Jacques M. Saint-Surin

) **SUPPLEMENTAL INFORMATION**
) **DISCLOSURE STATEMENT**

) 260 Sheridan Avenue, Suite 420
) Palo Alto, CA 94306
) (650) 833-0160

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The citations listed below, copies attached, may be material to the examination of the above-identified application, and are therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicant has become aware of the following printed publications which may be material to the examination of this application:

- U.S. Patent No. 3,566,271;
- U.S. Patent No. 3,943,305;
- U.S. Patent No. 4,677.667;
- U.S. Patent No. 4,894,860;
- U.S. Patent No. 4,984,262;
- U.S. Patent No. 4,991,196;
- U.S. Patent No. 5,018,184;
- U.S. Patent No. 5,073,919; and

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HAVERSTOCK & OWENS/LLP

Dated: 22/01/18 **By:** [Signature]

By:

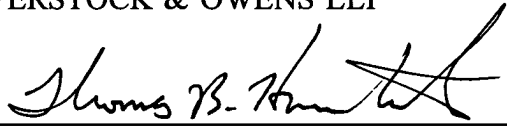
- Signetics Linear Products, TEA 1060/61 Product Specification, "Versatile Telephone Transmission Circuits with Dialer Interface" pages 6-1-2 to 6-112, August 1, 1988.

This Supplemental Information Disclosure Statement under 37 CFR §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination does not exist, or that any one or more of these citations constitutes prior art.

Respectfully submitted,

HAVERSTOCK & OWENS LLP

Dated: 5-28-98

By: 
Thomas B. Haverstock
Reg. No.: 32,571

Attorneys for Applicant